

Thermal Scanning Probe Lithography: Continuing the Path to Semiconductor Miniaturization

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Abstracts

REPORT INCLUDES:

Technological highlights and market outlook of thermal scanning probe lithography (tSPL) as a mean to semiconductor miniaturization

Insight into the role of tSPL process in diverse industry sectors, including electronics and optoelectronics; chemistry, biochemistry and healthcare; sensors and actuators; energy; and fabrication of 3D structures on transparent substrates etc.

Summary of lithographic processes for creating electronic features using deposition technologies for ultrathin films

An assessment of current trends, technology status, strengths and drawbacks, and emerging applications of tSPL at the industrial level



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Fabrication of Nanostructures
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