

Semiconductor Manufacturing International Corp. Fundamental Company Report Including Financial, SWOT, Competitors and Industry Analysis

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Abstracts

Semiconductor Manufacturing International Corp. Fundamental Company Report provides a complete overview of the company's affairs. All available data is presented in a comprehensive and easily accessed format. The report includes financial and SWOT information, industry analysis, opinions, estimates, plus annual and quarterly forecasts made by stock market experts. The report also enables direct comparison to be made between Semiconductor Manufacturing International Corp. and its competitors. This provides our Clients with a clear understanding of Semiconductor Manufacturing International Corp. position in the [Semiconductor](#) Industry.

The report contains detailed information about Semiconductor Manufacturing International Corp. that gives an unrivalled in-depth knowledge about internal business-environment of the company: data about the owners, senior executives, locations, subsidiaries, markets, products, and company history.

Another part of the report is a SWOT-analysis carried out for Semiconductor Manufacturing International Corp.. It involves specifying the objective of the company's business and identifies the different factors that are favorable and unfavorable to achieving that objective. SWOT-analysis helps to understand company's strengths, weaknesses, opportunities, and possible threats against it.

The Semiconductor Manufacturing International Corp. financial analysis covers the income statement and ratio trend-charts with balance sheets and cash flows

presented on an annual and quarterly basis. The report outlines the main financial ratios pertaining to profitability, margin analysis, asset turnover, credit ratios, and company's long-term solvency. This sort of company's information will assist and strengthen your company's decision-making processes.

In the part that describes Semiconductor Manufacturing International Corp. competitors and the industry in whole, the information about company's financial ratios is compared to those of its competitors and to the industry. The unique analysis of the market and company's competitors along with detailed information about the internal and external factors affecting the relevant industry will help to manage your business environment. Your company's business and sales activities will be boosted by gaining an insight into your competitors' businesses.

Also the report provides relevant news, an analysis of PR-activity, and stock price movements. The latter are correlated with pertinent news and press releases, and annual and quarterly forecasts are given by a variety of experts and market research firms. Such information creates your awareness about principal trends of Semiconductor Manufacturing International Corp. business.

About Semiconductor Manufacturing International Corp.

Semiconductor Manufacturing International Corporation engages in the computer-aided design, manufacturing, packaging, testing and trading of integrated circuits and other semiconductor services, as well as manufacturing and designing semiconductor masks.

In addition to wafer fabrication, the company's service offerings include a portfolio of intellectual property consisting of libraries and circuit design blocks, design support, mask-making, wafer probing, gold/solder bumping and redistribution layer manufacturing. It also works with its partners to provide assembly and testing services. The company has a global and diversified customer base that includes integrated device manufacturer (IDMs) and fabless semiconductor companies.

Fabs

In addition to its Shanghai mega-fab, the company has fabs at its Shanghai site. A portion of one facility in Shanghai is being leased to Toppan SMIC Electronics

(Shanghai) Co., Ltd., which manufactures color filters and micro-lenses for CMOS image sensors. It has one 8-inch fab under construction in Shenzhen. As of December 31, 2009, the company's fabs had an aggregate capacity of 162,050 8-inch wafer equivalents per month for wafer fabrication.

Services

Wafer Fabrication Services

The company provides semiconductor fabrication services using 0.35 micron to 65 nanometer technology for various devices, including logic technologies, including standard logic, mixed-signal, RF and high voltage circuits; memory technologies, including dynamic random access memory (DRAM), static random access memory (SRAM), flash, erasable programmable read-only memory (EEPROM) and Mask ROM; and specialty technologies, including liquid crystal on silicon (LCoS), and CMOS image sensor (CIS).

These semiconductors are used in various computing, communications, consumer and industrial applications, such as computers, mobile telephones, digital televisions, digital cameras, DVD players, entertainment devices, other consumer electronics devices, and automotive and industrial applications.

Semiconductors

The company manufactures various types of semiconductors, including logic semiconductors, mixed-signal and radio frequency (RF), high voltage, memory semiconductors, specialty semiconductors, LCoS, and CIS.

Integrated Solutions

In addition to wafer fabrication, the company provides its customers with a range of complementary services, from circuit design support and mask-making to wafer level probing and testing. This range of services is supported by its network of partners that assist in providing design, probing, final testing, packaging, assembly, and distribution services.

Design Support Services

The company's design support services include providing its customers with access to

the fundamental technology files and intellectual property libraries that facilitate customers' own integrated circuit design. It also offers design reference flows and access to its design center alliance, as well as layout services.

Libraries

The company offers libraries of compatible designs for portions of semiconductors, such as standard cells, Input/Output and selected memory blocks, in addition to technology files. It has arrangements with other providers of libraries to provide its customers with access to a library portfolio for their designs. In particular, the company offers a portfolio of application specific integrated circuit (ASIC) library and design kits for a range of tested and verified circuit applications and design-flow implementation. The company is developing additional libraries. Its library partners include ARM, Synopsys, Inc., VeriSilicon, and Virage Logic.

Intellectual Property

The company's intellectual property partners include ARM, MIPS, Virage, Synopsys, and Verisilicon.

Design Reference Flows

Customers implementing designs on the company's processes can utilize its design reference flows. These flows have been created using design tools developed by its electronic design automation partners, including Cadence Design Systems, Inc., Magma Design Automation, Inc., Mentor Graphics Corporation, and Synopsys, Inc.

Design Center Alliance

The company recommends design partners from its design services network.

Mask-making Services

The company's mask shop cooperates with its research and development department to develop new technologies and designs. Its mask-making facility, which is located in Shanghai, includes a 3,750 square meters clean room. Its facility is capable of producing binary masks, optical proximity correction masks and phase shift masks. Its mask facility also offers mask repair services. The company also offers a multi-project wafer service.

Wafer Probing, Assembly and Testing Services

The company has its own probing facilities in Shanghai and Beijing that provide test program development, probe card fabrication, wafer probing, failure analysis, and failure testing. It also outsources these services to its partners for those customers that request them. It has relationships with assembly and testing partners, including Amkor Assembly & Test (Shanghai) Co., Ltd. and ST Assembly Test Services Ltd.

Significant Events

On July 22, 2010, Virage Logic Corporation and Semiconductor Manufacturing International Corporation announced the extension of their longstanding partnership to the 40-nanometer (nm) low-leakage (LL) process technology.

In October 2010, The Wuhan East Lake Hi-Tech Development Zone Administrative Committee and Semiconductor Manufacturing International Corp. signed a cooperation framework agreement in Wuhan's East Lake Hotel in which both parties agree to cooperate and jointly invest in the 12-inch wafer production facilities of Wuhan Xinxin Semiconductor Manufacturing Corporation (Wuhan Xinxin) through cash injection.

Competition

The company's competitors include Taiwan Semiconductor Manufacturing Company, Limited (TSMC), United Microelectronics Corporation (UMC), and GlobalFoundries, as well as integrated device manufacturer (IDM) that include IBM.

History

Semiconductor Manufacturing International Corporation was founded in 2000.

The above Company Fundamental Report is a half-ready report and contents are subject to change.

It means that we have all necessary data in our database to prepare the report but need **2-3 days** to complete it. During this time we are also updating the report with respect to the current moment. So, you can get all the most recent data available for the same price. Please note that preparation of additional types of analyses requires extra time.

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1 – Data availability depends on company's security policy.

2 – These sections are available only when you purchase a report with appropriate additional types of analyses.

The complete financial data is available for publicly traded companies.

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ANALYSIS FEATURES

SWOT Analysis

SWOT, which stands for Strengths, Weaknesses, Opportunities and Threats, is an analytical framework that identifies the internal and external factors that are favorable and unfavorable for a company.

Enhanced SWOT Analysis

Enhanced SWOT is a 3×3 grid that arranges strengths, weaknesses, opportunities and threats into one scheme:

How to use the strengths to take advantage of the opportunities?

How to use the strengths to reduce likelihood and impact of the threats?

How to overcome the weaknesses that obstruct taking advantage of the opportunities?

How to overcome the weaknesses that can make the threats a reality?

Upon answering these questions a company can develop a project plan to improve its business performance.

PESTEL Analysis

PESTEL (also termed as PESTLE) is an ideal tool to strategically analyze what influence different outside factors – political, economic, sociocultural, technological, environmental and legal – exert on a business to later chart its long term targets.

Being part of the external analysis when carrying out a strategic assessment or performing a market study, PESTEL gives an overview of diverse macro-environmental factors that any company should thoughtfully consider. By perceiving these outside environments, businesses can maximally benefit from the opportunities while minimizing the threats to the organization.

Key Factors Examined by PESTEL Analysis:

Political – What opportunities and pressures are brought by political bodies and what is the degree of public regulations' impact on the business?

Economic – What economic policies, trends and structures are expected to affect the organization, what is this influence's degree?

Sociological – What cultural and societal aspects will work upon the demand for the business's products and operations?

Technological – What impact do the technological aspects, innovations, incentives and barriers have on the organization?

Environmental – What environmental and ecological facets, both locally and farther afield, are likely to predetermine the business?

Legal – What laws and legislation will exert influence on the style the business is carried out?

IFE, EFE, IE Matrices

The Internal Factor Evaluation matrix (IFE matrix) is a strategic management tool helping audit or evaluate major weaknesses and strengths in a business's functional areas. In addition, IFE matrix serves as a basis for identifying and assessing relationships amongst those areas. The IFE matrix is utilised in strategy formulation.

The External Factor Evaluation matrix (EFE matrix) is a tool of strategic management that is typically utilised to assess current market conditions. It is an ideal instrument for visualising and prioritising the threats and opportunities a firm is facing.

The essential difference between the above mentioned matrices lies in the type of factors incorporated in the model; whilst the latter is engaged in internal factors, the former deals exceptionally with external factors – those exposed to social, political, economic, legal, etc. external forces.

Being a continuation of the EFE matrix and IFE matrix models, the Internal External matrix (IE matrix) rests upon an investigation of external and internal business factors

integrated into one suggestive model.

Porter Five Forces Analysis

The Porter's five forces analysis studies the industry of operation and helps the company find new sources of competitive advantage. The analysis surveys an industry through five major questions:

What composes a threat of substitute products and services?

Is there a threat of new competitors entering the market?

What is the intensity of competitive rivalry?

How big is the bargaining power of buyers?

How significant is the bargaining power of suppliers?

VRIO Analysis

VRIO stands for Value, Rarity, Imitability, Organization. This analysis helps to evaluate all company's resources and capabilities and bring them together into one aggregate table that includes:

Tangible resources

Financial

Physical

Technological

Organizational

Intangible resources

Human

Innovation and Creativity

Reputation

Organizational capabilities

The result of the analysis gives a clear picture of company's competitive and economic implications, answering the questions if the resources mentioned above are:

Valuable?

Rare?

Costly to imitate?

Organized properly?

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