

Lattice Semiconductor Corporation Fundamental Company Report Including Financial, SWOT, Competitors and Industry Analysis

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Abstracts

Lattice Semiconductor Corporation Fundamental Company Report provides a complete overview of the company's affairs. All available data is presented in a comprehensive and easily accessed format. The report includes financial and SWOT information, industry analysis, opinions, estimates, plus annual and quarterly forecasts made by stock market experts. The report also enables direct comparison to be made between Lattice Semiconductor Corporation and its competitors. This provides our Clients with a clear understanding of Lattice Semiconductor Corporation position in the <u>Semiconductor</u> Industry.

The report contains detailed information about Lattice Semiconductor Corporation that gives an unrivalled in-depth knowledge about internal business-environment of the company: data about the owners, senior executives, locations, subsidiaries, markets, products, and company history.

Another part of the report is a SWOT-analysis carried out for Lattice Semiconductor Corporation. It involves specifying the objective of the company's business and identifies the different factors that are favorable and unfavorable to achieving that objective. SWOT-analysis helps to understand company's strengths, weaknesses, opportunities, and possible threats against it.

The Lattice Semiconductor Corporation financial analysis covers the income statement and ratio trend-charts with balance sheets and cash flows presented on an annual and quarterly basis. The report outlines the main financial ratios



pertaining to profitability, margin analysis, asset turnover, credit ratios, and company's long-term solvency. This sort of company's information will assist and strengthen your company's decision-making processes.

In the part that describes Lattice Semiconductor Corporation competitors and the industry in whole, the information about company's financial ratios is compared to those of its competitors and to the industry. The unique analysis of the market and company's competitors along with detailed information about the internal and external factors affecting the relevant industry will help to manage your business environment. Your company's business and sales activities will be boosted by gaining an insight into your competitors' businesses.

Also the report provides relevant news, an analysis of PR-activity, and stock price movements. The latter are correlated with pertinent news and press releases, and annual and quarterly forecasts are given by a variety of experts and market research firms. Such information creates your awareness about principal trends of Lattice Semiconductor Corporation business.

About Lattice Semiconductor Corporation

Lattice Semiconductor Corporation engages in the design, development, and marketing of programmable logic products and related software.

The company's programmable logic products are used semiconductor components that can be configured by end customers as specific logic circuits, enabling shorter design cycle times. Its end customers are primarily original equipment manufacturers (OEMs) in the communications, computing, consumer, industrial, automotive, medical and military end markets.

Lattice Products

LatticeECP Low-Power High-Value FPGAs (field programmable gate arrays): The LatticeECP family was designed for customers who need FPGAs with digital signal processing (DSP), a significant amount of memory, and high-speed serial communications channels. The LatticeECP family is able to serve this mid-range market due to circuit design choices. This family is suited for deployment in wireless infrastructure and wireline access equipment, as well as video and imaging applications.



All three generations of the LatticeECP family are manufactured using its foundry partner Fujitsu Limited's (Fujitsu) advanced process technologies.

LatticeXP Non-Volatile FPGAs: Lattice's two generations of the non-volatile LatticeXP FPGA family embed a Flash memory block on-chip to store the program. Both generations of the LatticeXP family are manufactured using embedded Flash processes co-developed with its foundry partner Fujitsu.

LatticeSC System Level FPGAs: The LatticeSC family combines a high performance FPGA fabric with features to meet the needs of high-speed communication system designs. These features include high-speed serial communication channels, large memories, and high-speed input/output (I/O).

PLD (programmable logic devices) Products

The company offers a line of PLDs based on its various families of ispLSI, ispMACH and GAL products.

ispMACH4000 CPLDs: Lattice offers seven versions of the ispMACH4000 in-system programmable CPLD (complex programmable logic device) family. The version, ispMach4000ZE, is designed for high performance and features an architecture optimized to ensure low power consumption. The ispMach4000ZE devices are targeted toward handheld and portable equipment.

MachXO PLDs: The MachXO family of non-volatile reconfigurable PLDs is designed for applications traditionally implemented using CPLDs or low-capacity FPGAs. Designed for a range of low density applications, the MachXO PLD family is used in various end markets including consumer, communications, computing, industrial and medical.

Power Manager and ispClock Programmable Mixed Signal Devices: The company's Power Manager and ispClock families feature a combination of programmable logic and programmable analog circuitry.

Software Development Tools and Intellectual Property Cores: The company's products are supported by the ispLEVER software development tool suite and PAC-Designer software. Supporting Windows, UNIX and LINUX platforms, ispLEVER software allows its customers to enter, verify and synthesize a design, perform logic simulation and timing analysis, assign I/O pins, designate critical paths, debug, execute automatic timing-driven place and route tasks, and download a logic and I/O configuration to its



devices.

Suppliers

Wafer Fabrication: The company sources silicon wafers from its foundry partners, Fujitsu in Japan, Seiko Epson Corporation in Japan, United Microelectronics Corporation in Taiwan and Chartered Semiconductor Manufacturing, Ltd. in Singapore.

Customers

The company's end customers are primarily original equipment manufacturers in the communications, computing, consumer, industrial, automotive, medical and military end markets. As of December 2009, the company has agreements with 17 manufacturers' representatives and four primary distributors; Arrow Electronics, Inc. (USA), Avnet Inc. (USA), distributors of the Weikeng Group (Weikeng Industrial Co. Ltd. (Taiwan) and Weikeng International Co. Ltd. (Hong Kong)), and Origin Electronics (a division of Nu Horizons Electronics Asia Pte. Ltd. (Singapore)). The company also has established export sales channels in approximately 50 foreign countries through a network of sales representatives and distributors.

Competition

The company competes directly with Actel Corporation, Altera Corporation and Xilinx, Inc.

History

Lattice Semiconductor Corporation was founded in 1983.

The above Company Fundamental Report is a half-ready report and contents are subject to change.

It means that we have all necessary data in our database to prepare the report but need **2-3 days** to complete it. During this time we are also updating the report with respect to the current moment. So, you can get all the most recent data available for the same price. Please note that preparation of additional types of analyses requires extra time.



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ANALYSIS FEATURES

SWOT Analysis

SWOT, which stands for Strengths, Weaknesses, Opportunities and Threats, is an analytical framework that identifies the internal and external factors that are favorable and unfavorable for a company.

Enhanced SWOT Analysis

Enhanced SWOT is a 3×3 grid that arranges strengths, weaknesses, opportunities and threats into one scheme:

How to use the strengths to take advantage of the opportunities?

How to use the strengths to reduce likelihood and impact of the threats?

How to overcome the weaknesses that obstruct taking advantage of the opportunities?

How to overcome the weaknesses that can make the threats a reality?

Upon answering these questions a company can develop a project plan to improve its business performance.

PESTEL Analysis

PESTEL (also termed as PESTLE) is an ideal tool to strategically analyze what influence different outside factors – political, economic, sociocultural, technological, environmental and legal – exert on a business to later chart its long term targets.

Being part of the external analysis when carrying out a strategic assessment or performing a market study, PESTEL gives an overview of diverse macro-environmental factors that any company should thoughtfully consider. By perceiving these outside environments, businesses can maximally benefit from the opportunities while minimizing the threats to the organization.



Key Factors Examined by PESTEL Analysis:

Political – What opportunities and pressures are brought by political bodies and what is the degree of public regulations' impact on the business?

Economic – What economic policies, trends and structures are expected to affect the organization, what is this influence's degree?

Sociological – What cultural and societal aspects will work upon the demand for the business's products and operations?

Technological – What impact do the technological aspects, innovations, incentives and barriers have on the organization?

Environmental – What environmental and ecological facets, both locally and farther afield, are likely to predetermine the business?

Legal – What laws and legislation will exert influence on the style the business is carried out?

IFE, EFE, IE Matrices

The Internal Factor Evaluation matrix (IFE matrix) is a strategic management tool helping audit or evaluate major weaknesses and strengths in a business's functional areas. In addition, IFE matrix serves as a basis for identifying and assessing relationships amongst those areas. The IFE matrix is utilised in strategy formulation.

The External Factor Evaluation matrix (EFE matrix) is a tool of strategic management that is typically utilised to assess current market conditions. It is an ideal instrument for visualising and prioritising the threats and opportunities a firm is facing.

The essential difference between the above mentioned matrices lies in the type of factors incorporated in the model; whilst the latter is engaged in internal factors, the former deals exceptionally with external factors – those exposed to social, political, economic, legal, etc. external forces.

Being a continuation of the EFE matrix and IFE matrix models, the Internal External matrix (IE matrix) rests upon an investigation of external and internal business factors



integrated into one suggestive model.

Porter Five Forces Analysis

The Porter's five forces analysis studies the industry of operation and helps the company find new sources of competitive advantage. The analysis surveys an industry through five major questions:

What composes a threat of substitute products and services?

Is there a threat of new competitors entering the market?

What is the intensity of competitive rivalry?

How big is the bargaining power of buyers?

How significant is the bargaining power of suppliers?

VRIO Analysis

VRIO stands for Value, Rarity, Imitability, Organization. This analysis helps to evaluate all company's resources and capabilities and bring them together into one aggregate table that includes:

Tangible resources

Financial

Physical

Technological

Organizational

Intangible resources

Human

Innovation and Creativity



Reputation

Organizational capabilities

The result of the analysis gives a clear picture of company's competitive and economic implications, answering the questions if the resources mentioned above are:

Valuable?
Rare?
Costly to imitate?
Organized properly?



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