

# Polished Silicon Wafer Market Status and Trend Analysis 2017-2026 (COVID-19 Version)

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## Abstracts

### SUMMARY

Further key aspects of the report indicate that:

Chapter 1: Research Scope: Product Definition, Type, End-Use & Methodology

Chapter 2: Global Industry Summary

Chapter 3: Market Dynamics

Chapter 4: Global Market Segmentation by region, type and End-Use

Chapter 5: North America Market Segmentation by region, type and End-Use

Chapter 6: Europe Market Segmentation by region, type and End-Use

Chapter 7: Asia-Pacific Market Segmentation by region, type and End-Use

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Chapter 9: Middle East and Africa Market Segmentation by region, type and End-Use.

Chapter 10: Market Competition by Companies

Chapter 11: Market forecast and environment forecast.

Chapter 12: Industry Summary.

The global Polished Silicon Wafer market has the potential to grow with xx million USD with growing CAGR in the forecast period from 2021f to 2026f. Factors driving the market for @@@@ are the significant development of demand and improvement of COVID-19 and geo-economics.

Based on the type of product, the global Polished Silicon Wafer market segmented into

300 mm

200 mm

150 mm

125 mm

Others

Based on the end-use, the global Polished Silicon Wafer market classified into

Memory

Logic/MPU

Others

Based on geography, the global Polished Silicon Wafer market segmented into

North America [U.S., Canada, Mexico]

Europe [Germany, UK, France, Italy, Rest of Europe]

Asia-Pacific [China, India, Japan, South Korea, Southeast Asia, Australia, Rest of Asia Pacific]

South America [Brazil, Argentina, Rest of Latin America]

Middle East & Africa [GCC, North Africa, South Africa, Rest of Middle East and Africa]

And the major players included in the report are

Shin-Etsu Chemical

SUMCO CORPORATION

GlobalWafers

SK Siltron Co., Ltd.

Siltronic

Okmetic

Ferrotec

JRH

Wafer Works

Simgui

Poshing

GRITEK

Zhonghuan Huanou

MCL

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