

Global Polished Silicon Wafer Market Analysis 2015-2019 and Forecast 2020-2025

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Abstracts

SNAPSHOT

The global Polished Silicon Wafer market size is estimated at xxx million USD with a CAGR xx% from 2015-2019 and is expected to reach xxx Million USD in 2020 with a CAGR xx% from 2020 to 2025. The report begins from overview of Industry Chain structure, and describes industry environment, then analyses market size and forecast of Polished Silicon Wafer by product, region and application, in addition, this report introduces market competition situation among the vendors and company profile, besides, market price analysis and value chain features are covered in this report.

Product Type Coverage (Market Size & Forecast, Major Company of Product Type etc.):

300 mm

200 mm

150 mm

125 mm

Others

Company Coverage (Company Profile, Sales Revenue, Price, Gross Margin, Main Products etc.):

Shin-Etsu Chemical

SUMCO CORPORATION

GlobalWafers

SK Siltron Co., Ltd.

Siltronic

Okmetic

Ferrotec

JRH

Wafer Works

Simgui

Poshing

GRITEK

Zhonghuan Huanou

MCL

Application Coverage (Market Size & Forecast, Different Demand Market by Region, Main Consumer Profile etc.):

Memory

Logic/MPU

Others

Region Coverage (Regional Production, Demand & Forecast by Countries etc.):

North America (U.S., Canada, Mexico)

Europe (Germany, U.K., France, Italy, Russia, Spain etc.)

Asia-Pacific (China, India, Japan, Southeast Asia etc.)

South America (Brazil, Argentina etc.)

Middle East & Africa (Saudi Arabia, South Africa etc.)

Contents

1 INDUSTRY OVERVIEW

1.1 Polished Silicon Wafer Industry

Figure Polished Silicon Wafer Industry Chain Structure

1.1.1 Overview

1.1.2 Development of Polished Silicon Wafer

1.2 Market Segment

1.2.1 Upstream

Table Upstream Segment of Polished Silicon Wafer

1.2.2 Downstream

Table Application Segment of Polished Silicon Wafer

Table Global Polished Silicon Wafer Market 2015-2025, by Application, in USD Million

1.3 Cost Analysis

2 INDUSTRY ENVIRONMENT (PEST ANALYSIS)

2.1 Policy

2.2 Economics

2.3 Sociology

2.4 Technology

3 POLISHED SILICON WAFER MARKET BY TYPE

3.1 By Type

3.1.1 300 mm

Table Major Company List of 300 mm

3.1.2 200 mm

Table Major Company List of 200 mm

3.1.3 150 mm

Table Major Company List of 150 mm

3.1.4 125 mm

Table Major Company List of 125 mm

3.1.5 Others

Table Major Company List of Others

3.2 Market Size

Table Global Polished Silicon Wafer Market 2015-2019, by Type, in USD Million

Figure Global Polished Silicon Wafer Market Growth 2015-2019, by Type, in USD

Million

Table Global Polished Silicon Wafer Market 2015-2019, by Type, in Volume

Figure Global Polished Silicon Wafer Market Growth 2015-2019, by Type, in Volume

3.3 Market Forecast

Table Global Polished Silicon Wafer Market Forecast 2020-2025, by Type, in USD
Million

Table Global Polished Silicon Wafer Market Forecast 2020-2025, by Type, in Volume

4 MAJOR COMPANIES LIST

4.1 Shin-Etsu Chemical (Company Profile, Sales Data etc.)

4.1.1 Shin-Etsu Chemical Profile

Table Shin-Etsu Chemical Overview List

4.1.2 Shin-Etsu Chemical Products & Services

4.1.3 Shin-Etsu Chemical Business Operation Conditions

Table Business Operation of Shin-Etsu Chemical (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.2 SUMCO CORPORATION (Company Profile, Sales Data etc.)

4.2.1 SUMCO CORPORATION Profile

Table SUMCO CORPORATION Overview List

4.2.2 SUMCO CORPORATION Products & Services

4.2.3 SUMCO CORPORATION Business Operation Conditions

Table Business Operation of SUMCO CORPORATION (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.3 GlobalWafers (Company Profile, Sales Data etc.)

4.3.1 GlobalWafers Profile

Table GlobalWafers Overview List

4.3.2 GlobalWafers Products & Services

4.3.3 GlobalWafers Business Operation Conditions

Table Business Operation of GlobalWafers (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.4 SK Siltron Co., Ltd. (Company Profile, Sales Data etc.)

4.4.1 SK Siltron Co., Ltd. Profile

Table SK Siltron Co., Ltd. Overview List

4.4.2 SK Siltron Co., Ltd. Products & Services

4.4.3 SK Siltron Co., Ltd. Business Operation Conditions

Table Business Operation of SK Siltron Co., Ltd. (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.5 Siltronic (Company Profile, Sales Data etc.)

4.5.1 Siltronic Profile

Table Siltronic Overview List

4.5.2 Siltronic Products & Services

4.5.3 Siltronic Business Operation Conditions

Table Business Operation of Siltronic (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.6 Okmetic (Company Profile, Sales Data etc.)

4.6.1 Okmetic Profile

Table Okmetic Overview List

4.6.2 Okmetic Products & Services

4.6.3 Okmetic Business Operation Conditions

Table Business Operation of Okmetic (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.7 Ferrotec (Company Profile, Sales Data etc.)

4.7.1 Ferrotec Profile

Table Ferrotec Overview List

4.7.2 Ferrotec Products & Services

4.7.3 Ferrotec Business Operation Conditions

Table Business Operation of Ferrotec (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.8 JRH (Company Profile, Sales Data etc.)

4.8.1 JRH Profile

Table JRH Overview List

4.8.2 JRH Products & Services

4.8.3 JRH Business Operation Conditions

Table Business Operation of JRH (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.9 Wafer Works (Company Profile, Sales Data etc.)

4.9.1 Wafer Works Profile

Table Wafer Works Overview List

4.9.2 Wafer Works Products & Services

4.9.3 Wafer Works Business Operation Conditions

Table Business Operation of Wafer Works (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.10 Simgui (Company Profile, Sales Data etc.)

4.10.1 Simgui Profile

Table Simgui Overview List

4.10.2 Simgui Products & Services

4.10.3 Simgui Business Operation Conditions

Table Business Operation of Simgui (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.11 Poshing (Company Profile, Sales Data etc.)

4.11.1 Poshing Profile

Table Poshing Overview List

4.11.2 Poshing Products & Services

4.11.3 Poshing Business Operation Conditions

Table Business Operation of Poshing (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

4.12 GRITEK (Company Profile, Sales Data etc.)

4.12.1 GRITEK Profile

Table GRITEK Overview List

4.12.2 GRITEK Products & Services

4.12.3 GRITEK Business Operation Conditions

Table Business Operation of GRITEK (Sales Revenue, Cost, Gross Margin)

4.13 Zhonghuan Huanou (Company Profile, Sales Data etc.)

4.13.1 Zhonghuan Huanou Profile

Table Zhonghuan Huanou Overview List

4.13.2 Zhonghuan Huanou Products & Services

4.13.3 Zhonghuan Huanou Business Operation Conditions

Table Business Operation of Zhonghuan Huanou (Sales Revenue, Cost, Gross Margin)

4.14 MCL (Company Profile, Sales Data etc.)

4.14.1 MCL Profile

Table MCL Overview List

4.14.2 MCL Products & Services

4.14.3 MCL Business Operation Conditions

Table Business Operation of MCL (Sales Revenue, Cost, Gross Margin)

5 MARKET COMPETITION

5.1 Company Competition

Table Global Polished Silicon Wafer Sales Revenue 2015-2019, by Company, in USD Million

Table Global Polished Silicon Wafer Sales Revenue Share 2015-2019, by Company, in USD Million

Figure Global Polished Silicon Wafer Sales Revenue Share in 2019, by Company, in USD Million

Table Global Polished Silicon Wafer Sales Volume 2015-2019, by Company, in Volume

Table Global Polished Silicon Wafer Sales Volume Share 2015-2019, by Company, in

Volume

Figure Global Polished Silicon Wafer Sales Volume Share in 2019, by Company, in Volume

5.2 Regional Market by Company

Figure North America Polished Silicon Wafer Market Concentration, in 2019

Figure Europe Polished Silicon Wafer Market Market Concentration, in 2019

Figure Asia-Pacific Polished Silicon Wafer Market Concentration, in 2019

Figure South America Polished Silicon Wafer Market Concentration, in 2019

Figure Middle East & Africa Polished Silicon Wafer Market Concentration, in 2019

6 DEMAND BY END MARKET

6.1 Demand Situation

6.1.1 Demand in Memory

Figure Polished Silicon Wafer Demand in Memory, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Memory, 2015-2019, in Volume

6.1.2 Demand in Logic/MPU

Figure Polished Silicon Wafer Demand in Logic/MPU, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Logic/MPU, 2015-2019, in Volume

6.1.3 Demand in Others

Figure Polished Silicon Wafer Demand in Others, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Others, 2015-2019, in Volume

6.2 Regional Demand Comparison

Table Regional Demand Comparison List

Table Major Application in Different Regions

6.3 Demand Forecast

Table Polished Silicon Wafer Demand Forecast 2020-2025, by Application, in USD Million

Figure Polished Silicon Wafer Market Growth 2020-2025, by Application, in USD Million

Figure Polished Silicon Wafer Market Share in 2025, by Application, in USD Million

Table Polished Silicon Wafer Demand Forecast 2020-2025, by Application, in Volume

Table Polished Silicon Wafer Market Growth 2020-2025, by Application, in Volume

Table Polished Silicon Wafer Market Share in 2025, by Application, in Volume

7 REGION OPERATION

7.1 Regional Production

Table Polished Silicon Wafer Production 2015-2019, by Region, in USD Million

Table Polished Silicon Wafer Production 2015-2019, by Region, in Volume

7.2 Regional Market

Table Global Polished Silicon Wafer Market 2015-2019, by Region, in USD Million

Table Global Polished Silicon Wafer Market Share 2015-2019, by Region, in USD Million

Table Global Polished Silicon Wafer Market 2015-2019, by Region, in Volume

Table Global Polished Silicon Wafer Market Share 2015-2019, by Region, in Volume

7.3 by Region

7.3.1 North America

7.3.1.1 Overview

Figure North America Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure North America Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

7.3.1.2 by Country (U.S., Canada, Mexico)

Table North America Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table North America Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

7.3.2 Europe

7.3.2.1 Overview

Figure Europe Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure Europe Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

7.3.2.2 by Country (Germany, U.K., France, Italy, Russia, Spain etc.)

Table Europe Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Europe Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

7.3.3 Asia-Pacific

7.3.3.1 Overview

Figure Asia-Pacific Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure Asia-Pacific Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

7.3.3.2 by Country (China, India, Japan, Southeast Asia etc.)

Table Asia-Pacific Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Asia-Pacific Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

7.3.4 South America

7.3.4.1 Overview

Figure South America Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure South America Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

7.3.4.2 by Country (Brazil, Argentina etc.)

Table South America Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table South America Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

7.3.5 Middle East & Africa

7.3.5.1 Overview

Figure Middle East & Africa Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure Middle East & Africa Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

7.3.5.2 by Country (Saudi Arabia, South Africa etc.)

Table Middle East & Africa Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Middle East & Africa Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

7.4 Regional Import & Export

7.5 Regional Forecast

Table Polished Silicon Wafer Market Forecast 2020-2025, by Region, in USD Million

Table Polished Silicon Wafer Market Forecast 2020-2025, by Region, in Volume

8 MARKETING & PRICE

8.1 Price and Margin

8.1.1 Price Trends

8.1.2 Factors of Price Change

Table Price Factors List

8.1.3 Manufacturers Gross Margin Analysis

8.2 Marketing Channel

Figure Marketing Channels Overview

9 RESEARCH CONCLUSION

List Of Tables

LIST OF TABLES

- Table Upstream Segment of Polished Silicon Wafer
- Table Application Segment of Polished Silicon Wafer
- Table Global Polished Silicon Wafer Market 2015-2025, by Application, in USD Million
- Table Major Company List of 200 mm
- Table Major Company List of 150 mm
- Table Major Company List of 125 mm
- Table Major Company List of Others
- Table Global Polished Silicon Wafer Market 2015-2019, by Type, in USD Million
- Table Global Polished Silicon Wafer Market 2015-2019, by Type, in Volume
- Table Global Polished Silicon Wafer Market Forecast 2020-2025, by Type, in USD Million
- Table Global Polished Silicon Wafer Market Forecast 2020-2025, by Type, in Volume
- Table Shin-Etsu Chemical Overview List
- Table Business Operation of Shin-Etsu Chemical (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table SUMCO CORPORATION Overview List
- Table Business Operation of SUMCO CORPORATION (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table GlobalWafers Overview List
- Table Business Operation of GlobalWafers (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table SK Siltron Co., Ltd. Overview List
- Table Business Operation of SK Siltron Co., Ltd. (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table Siltronic Overview List
- Table Business Operation of Siltronic (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table Okmetic Overview List
- Table Business Operation of Okmetic (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table Ferrotec Overview List
- Table Business Operation of Ferrotec (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)
- Table JRH Overview List
- Table Business Operation of JRH (Sales Revenue, Sales Volume, Price, Cost, Gross

Margin)

Table Wafer Works Overview List

Table Business Operation of Wafer Works (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

Table Simgui Overview List

Table Business Operation of Simgui (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

Table Poshing Overview List

Table Business Operation of Poshing (Sales Revenue, Sales Volume, Price, Cost, Gross Margin)

Table GRITEK Overview List

Table Business Operation of GRITEK (Sales Revenue, Cost, Gross Margin)

Table Zhonghuan Huanou Overview List

Table Business Operation of Zhonghuan Huanou (Sales Revenue, Cost, Gross Margin)

Table MCL Overview List

Table Business Operation of MCL (Sales Revenue, Cost, Gross Margin)

Table Global Polished Silicon Wafer Sales Revenue 2015-2019, by Company, in USD Million

Table Global Polished Silicon Wafer Sales Revenue Share 2015-2019, by Company, in USD Million

Table Global Polished Silicon Wafer Sales Volume 2015-2019, by Company, in Volume

Table Global Polished Silicon Wafer Sales Volume Share 2015-2019, by Company, in Volume

Table Regional Demand Comparison List

Table Major Application in Different Regions

Table Polished Silicon Wafer Demand Forecast 2020-2025, by Application, in USD Million

Table Polished Silicon Wafer Demand Forecast 2020-2025, by Application, in Volume

Table Polished Silicon Wafer Market Growth 2020-2025, by Application, in Volume

Table Polished Silicon Wafer Market Share in 2025, by Application, in Volume

Table Polished Silicon Wafer Production 2015-2019, by Region, in USD Million

Table Polished Silicon Wafer Production 2015-2019, by Region, in Volume

Table Global Polished Silicon Wafer Market 2015-2019, by Region, in USD Million

Table Global Polished Silicon Wafer Market Share 2015-2019, by Region, in USD Million

Table Global Polished Silicon Wafer Market 2015-2019, by Region, in Volume

Table Global Polished Silicon Wafer Market Share 2015-2019, by Region, in Volume

Table North America Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table North America Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

Table Europe Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Europe Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

Table Asia-Pacific Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Asia-Pacific Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

Table South America Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table South America Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

Table Middle East & Africa Polished Silicon Wafer Market Size 2015-2019, by Country, in USD Million

Table Middle East & Africa Polished Silicon Wafer Market Size 2015-2019, by Country, in Volume

Table Polished Silicon Wafer Market Forecast 2020-2025, by Region, in USD Million

Table Polished Silicon Wafer Market Forecast 2020-2025, by Region, in Volume

Table Price Factors List

List Of Figures

LIST OF FIGURES

Figure Polished Silicon Wafer Industry Chain Structure

Figure Global Polished Silicon Wafer Market Growth 2015-2019, by Type, in USD Million

Figure Global Polished Silicon Wafer Market Growth 2015-2019, by Type, in Volume

Figure Global Polished Silicon Wafer Sales Revenue Share in 2019, by Company, in USD Million

Figure Global Polished Silicon Wafer Sales Volume Share in 2019, by Company, in Volume

Figure North America Polished Silicon Wafer Market Concentration, in 2019

Figure Europe Polished Silicon Wafer Market Market Concentration, in 2019

Figure Asia-Pacific Polished Silicon Wafer Market Concentration, in 2019

Figure South America Polished Silicon Wafer Market Concentration, in 2019

Figure Middle East & Africa Polished Silicon Wafer Market Concentration, in 2019

Figure Polished Silicon Wafer Demand in Memory, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Memory, 2015-2019, in Volume

Figure Polished Silicon Wafer Demand in Logic/MPU, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Logic/MPU, 2015-2019, in Volume

Figure Polished Silicon Wafer Demand in Others, 2015-2019, in USD Million

Figure Polished Silicon Wafer Demand in Others, 2015-2019, in Volume

Figure Polished Silicon Wafer Market Growth 2020-2025, by Application, in USD Million

Figure Polished Silicon Wafer Market Share in 2025, by Application, in USD Million

Figure North America Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure North America Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

Figure Europe Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure Europe Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

Figure Asia-Pacific Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure Asia-Pacific Polished Silicon Wafer Market Size and Growth 2015-2019, in Volume

Figure South America Polished Silicon Wafer Market Size and Growth 2015-2019, in USD Million

Figure South America Polished Silicon Wafer Market Size and Growth 2015-2019, in

Volume

Figure Middle East & Africa Polished Silicon Wafer Market Size and Growth 2015-2019,
in USD Million

Figure Middle East & Africa Polished Silicon Wafer Market Size and Growth 2015-2019,
in Volume

Figure Marketing Channels Overview

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